TOSHIBA

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VHCT08AF,TC74VHCT08AFN,TC74VHCT08AFT,TC74VHCT08AFK

Quad 2-Input AND Gate

The TC74VHCT08A is an advanced high speed CMOS 2-INPUT AND GATE fabricated with silicon gate C²MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

The input voltage are compatible with TTL output voltage. This device may be used as a level converter for interfacing 3.3 V to 5 V system.

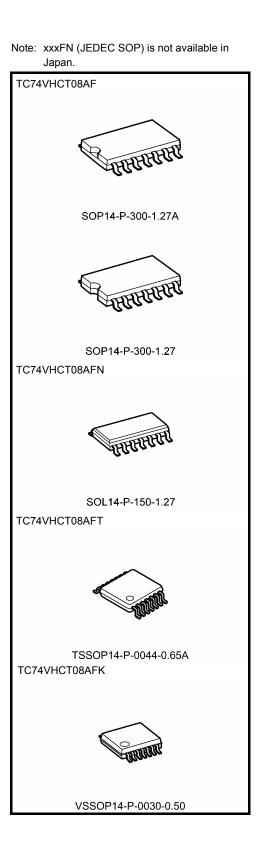
Input protection and output circuit ensure that 0 to 5.5 V can be applied to the input and output ^(Note) pins without regard to the supply voltage. These structure prevents device destruction due to mismatched supply and input/output voltages such as battery back up, hot board insertion, etc.

Note: $V_{CC} = 0 V$

Features

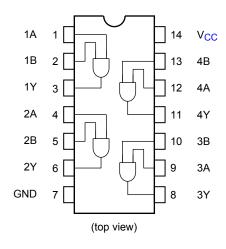
- High speed: $t_{pd} = 5.0 \text{ ns}$ (typ.) at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 2 \mu A (max)$ at $Ta = 25^{\circ}C$
- Compatible with TTL outputs: $V_{IL} = 0.8 V (max)$
- $V_{IH} = 2.0 V (min)$
- Power down protection is provided on all inputs and outputs.
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Low noise: VOLP = 0.8 V (max)
- Pin and function compatible with the 74 series (74AC/HC/F/ALS/LS etc.) 08 type.

Weight	
SOP14-P-300-1.27A	: 0.18 g (typ.)
SOP14-P-300-1.27	: 0.18 g (typ.)
SOL14-P-150-1.27	: 0.12 g (typ.)
TSSOP14-P-0044-0.65A	: 0.06 g (typ.)
VSSOP14-P-0030-0.50	: 0.02 g (typ.)

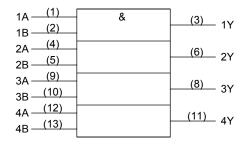


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Pin Assignment



IEC Logic Symbol



Truth Table

А	В	Y
L	L	L
L	Н	L
Н	L	L
Н	Н	Н

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to 7.0	V
	N/	-0.5 to 7.0 (Note 2)	V
DC output voltage	Vout	-0.5 to V _{CC} + 0.5 (Note 3)	
Input diode current	IIК	-20	mA
Output diode current	IOK	±20 (Note 4)	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	180	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Note 2: $V_{CC} = 0 V$

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4: $V_{OUT} < GND, V_{OUT} > V_{CC}$

Recommended Operating Conditions (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	4.5 to 5.5	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	Vour	0 to 5.5 (Note 2)	V
Output voltage	Vout	0 to V _{CC} (Note 3)	v
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dV	0 to 20	ns/V

Note 1: The recommended operating conditions are required to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Note 2: $V_{CC} = 0 V$

Note 3: High or low state.

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
	,			$V_{CC}(V)$	Min	Тур.	Max	Min	Max	
High-level input voltage	V _{IH}	—		4.5 to 5.5	2.0	_		2.0	_	V
Low-level input voltage	V _{IL}	—		4.5 to 5.5		_	0.8		0.8	V
High-level output	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -50 μA	4.5	4.40	4.50		4.40	_	V
voltage			$I_{OH} = -8 \text{ mA}$	4.5	3.94			3.80	_	v
Low-level output	V _{OL}	$V_{IN} = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 50 \ \mu A$	4.5	—	0.0	0.1	_	0.1	V
voltage			I _{OL} = 8 mA	4.5	_	_	0.36		0.44	
Input leakage current	IIN	V _{IN} = 5.5 V or GND		0 to 5.5		_	±0.1		±1.0	μA
	ICC	$V_{IN} = V_{CC}$ or GND		5.5			2.0		20.0	μA
Quiescent supply current	Ісст	Per input: $V_{IN} = 3.4 V$ Other input: V_{CC} or GND		5.5	_	_	1.35		1.50	mA
Output leakage current	I _{OPD}	V _{OUT} = 5.5 V		0	_		0.5		5.0	μA

AC Characteristics (input: tr = tf = 3 ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit	
			V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	Max
Propagation delay time	t _{pLH}		5.0 ± 0.5	15	_	5.0	6.9	1.0	8.0	ns
	t _{pHL}			50	_	5.5	7.9	1.0	9.0	115
Input capacitance	C _{IN}		_		_	4	10	_	10	pF
Power dissipation capacitance	C _{PD}			(Note)		18			_	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4$ (per gate)

Noise Characteristics (input: $t_r = t_f = 3 \text{ ns}$)

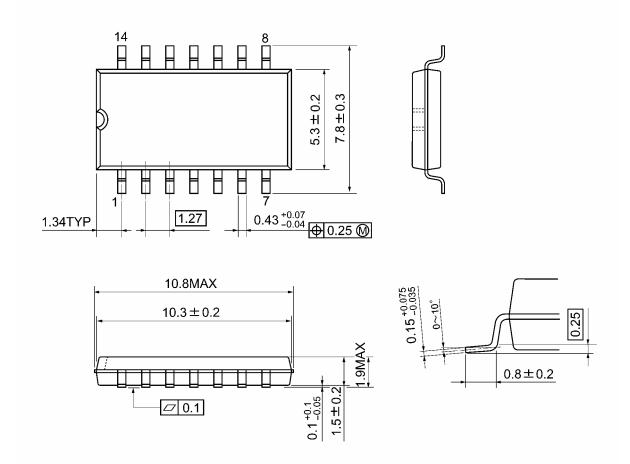
Characteristics	Symbol	Test Condition	Ta =	Unit		
Characteristics	Symbol		V _{CC} (V)	Тур.	Max	Offic
Quiet output maximum dynamic V_{OL}	V _{OLP}	$C_L = 50 \text{ pF}$	5.0	0.4	0.8	V
Quiet output minimum dynamic V_{OL}	V _{OLV}	C _L = 50 pF	5.0	-0.4	-0.8	V
Minimum high level dynamic input voltage	VIHD	C _L = 50 pF	5.0	_	2.0	V
Maximum low level dynamic input voltage	V _{ILD}	C _L = 50 pF	5.0		0.8	V



Package Dimensions

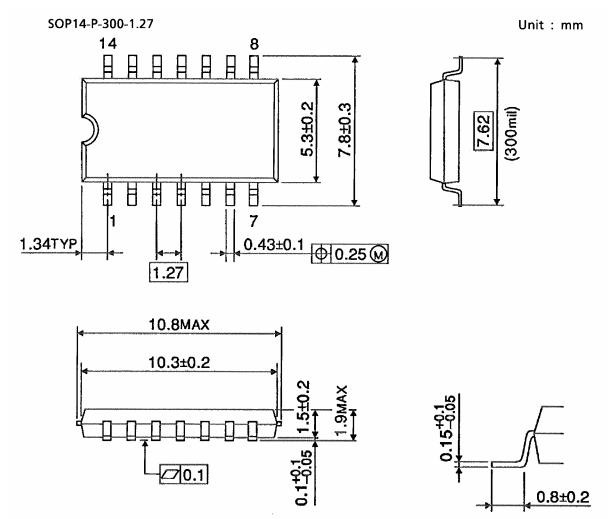
SOP14-P-300-1.27A

Unit: mm



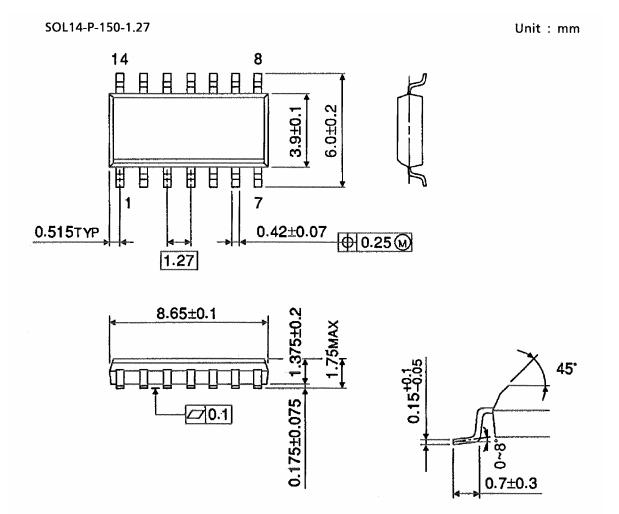
Weight: 0.18 g (typ.)

Package Dimensions



Weight: 0.18 g (typ.)

Package Dimensions (Note)



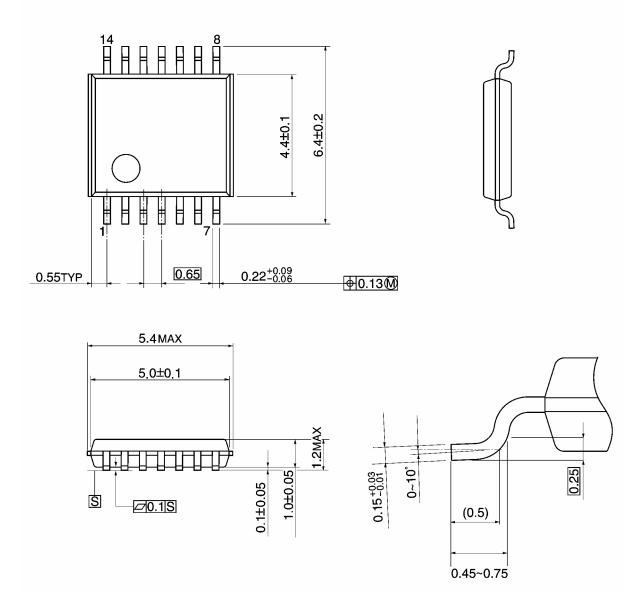
Note: This package is not available in Japan.

Weight: 0.12 g (typ.)

Package Dimensions

TSSOP14-P-0044-0.65A

Unit: mm



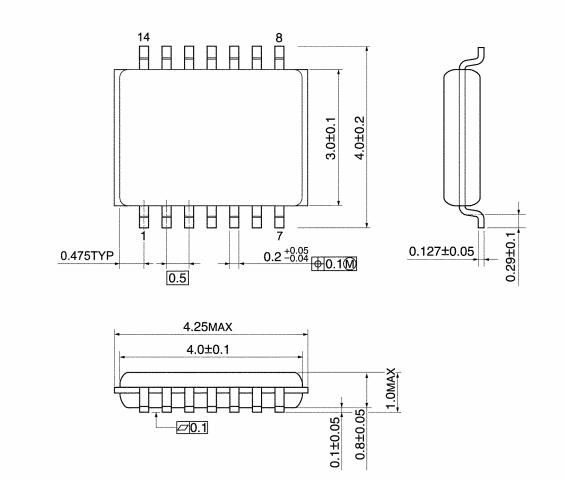
Weight: 0.06 g (typ.)

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Package Dimensions

VSSOP14-P-0030-0.50

Unit: mm



Weight: 0.02 g (typ.)

Note: Lead (Pb)-Free Packages

SOP14-P-300-1.27A SOL14-P-150-1.27 TSSOP14-P-0044-0.65A VSSOP14-P-0030-0.50

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